Hardware Manual
for
eNet-MA4

Part Number: 151B3-00000-A14
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CUSTOMER NOTES:

Revision Control History

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Note to the Reader and End-User:

Contact:

We welcome comments and suggestions. Please contact us at 888-429-1553 (toll free in US) or 505-994-3111 or visit our web site for support submit forms at www.altadt.com.

eNet-MA4
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eNet-MA4 Hardware Manual

Introduction

This manual provides detailed hardware information on the eNet-MA4 interface product.

In addition to this information, the reader may also want to reference the following documents provided on the CD and our Web Site

- **AltaCore**™ Specifications and User Manual: Detailed description of the MIL-STD-1553 protocol engine of the card and the A429 protocol engine of the card. Most people do not need this detail and will mainly reference the **AltaAPI** manual for their application development.
- **AltaAPI**™ User's Manual: Detailed description of the application program interface (API) and device drivers of this software package.
- **AltaView**™ User's Manual: AltaView is the latest MIL-STD-1553 analyzer and A429 analyzer on the market and this manual details the usage of the product.
- **AltaRTVal**™ User's Manual: This manual details the usage of AltaRTVal, which is an automated program to run AS4111/4112 RT Validation and Production Test Plans.
- 1553 Tutorial and Reference, and MIL-STD-1553B Standard. These documents provide a detailed review of the MIL-STD-1553 standard, which is required for proper usage of this product. **SEE THIS DOCUMENT FOR WIRE & CABLING INFORMATION OF 1553 BUSSES – THIS IS REQUIRED FOR PROPER BUS OPERATIONS.**
- A429 Tutorial and Reference. This document provides a review of A429 and other various ARINC standards.

ESD and General Handling of Computer Interface Cards

The Alta warranty requires that the product be handled with proper ESD controls. The JEDEC standard on ESD handling, JESD625, is available for free download at www.jedec.org. Please follow the standard's guideline for proper ESD handling methods. At a minimum the following guidelines should be followed:

- Avoid carpets in cool, dry areas.
- Leave the card in its anti-static packaging until ready to be installed.
- Dissipate static electricity before handling the card by touching a grounded metal object, such as the metal chassis of the system (the system should be plugged-in, but turned-off).
- Use antistatic devices, such as wrist straps and floor mats.
- Always hold the card by its edges. Avoid touching the components or connectors.
- Be sure to align card edge or assembly cable connector pins before installation. Misaligned connectors can cause damage to the card or system, especially at power-on.
- Take care when connecting or disconnecting cables. When disconnecting a cable, always pull on the cable connector, not on the cable itself.

eNet-MA4 Description

eNet-MA4 is an innovative product that provides "remoting" of 1553 and A429 operations on 10/100/1000 Ethernet IP/UDP local area networks (LAN). eNet-MA4 is a small, low-power, rugged device that provides connectivity for up to two dual redundant 1553 bus and 8 A429 channels and is ideal for remoting 1553 and A429 connections for in-field applications or point-point lab usage.

Alta has combined the industry's most advanced 32-bit 1553 and A429 FPGA protocol engines, ***AltaCore***™, with a real-time IP/UDP thin server. The customer can implement their application with the same feature-rich application programming interface, ***AltaAPI***™, which is a multi-layer ANSI C and Windows.NET (MSVS C++, C#, VB .NET) architecture. This hardware and software package provides increased system performance and reduces integration time.

Card Level Specifications

- 10/100/1000 Ethernet
- Supported protocols are UDP and ARP
- Programmable IP address: **Default IP address is 192.168.0.128**
- One or Two MIL-STD-1553 Channel(s)
    - Transformer Coupled Stub Interface Standard
    - Direct Coupled Stub Interface available upon request
- Up to 8 ARINC Channels – Various RX/TX Shared and Dedicated RX Channels
    - See Your Getting Started Sheet for Model Number
    - Standard ARINC-419/429/575/573/717 Configurations Include:
        - 4 RX/TX Shared Channels
        - 4 RX/TX Shared & 4 RX Channels
        - Note: Shared RX/TX channels could have severe RX voltage drain when not powered. Use dedicated RX only channels for embedded or critical systems. (See A429 Shared Channels Section for more detail.)

      For Shared Channels, RX Function is Always Available and TX is Software Selectable. Channels Can Be Fixed on Request.
- A/D Signal Capture on First Two RX Channels
- Dual ARINC-717 RX/TX Shared
    - Replaces Corresponding 429 Channels
- 3 Mbyte of on-board memory
- IRIG-B Receiver (DC or AM)
- Signal Capture capability
- 2 Single-Ended Bi-Directional Avionics Discrete I/O
- One LVTTL Input and Output Trigger
- External Input and Output Clocks (LVTTL)
- 1760 Ext RT Addressing (Resistor Hard-wire Build Option)
- Two Temperature Sensors
- Auto Load BC, RT and BM Images for Fast Startup
- Auto BM Mode for 1553 -> Ethernet Bridging
- Auto Load TX or RX data structure images for Fast Startup
- Auto Monitor Mode for A429 -> Ethernet Bridging
- 5-32 VDC Conditioned Power
    - Lab Use 5V USB Power 1.5 AMP
    - Embedded applications, use conditioned power source
    - Reverse Polarity Protected to 40 Volts
- 7.5 Watts Max (99% Bus Loading)
- Weight: 215g
- Operating Temperature range:
    - 0 to +70C Standard
    - -40 to +85C Extended Temp Parts with –E Option (as applicable).
- Relative humidity: 5 to 95% (non-condensing).
- RoHS Compliant

Power Specifications

Table 1. Idle Power

IDLE			
Channel	Input Voltage (V)	Current (A)	Total Power (W)
N/A	5	0.44	2.20

Table 2. 50% Bus Loading Power

50% Bus Loading			
#Channels	Input Voltage (V)	Current (A)	Total Power (W)
1	5	0.66	3.30
2	5	0.86	4.30

Table 3. 95% Bus Loading Power

95% Bus Loading			
#Channels	Input Voltage (V)	Current (A)	Power (W)
1	5	0.85	4.25
2	5	1.31	6.55

The power shown above is for a 1000Mbps link. 100Mbps operation is approximately 400mW under 1000Mbps. 10Mbps power is similar to 100Mbps, but this operation is not recommended.

A429 adds approximately 100mW with 4 transmit channels running one label at 1ms to one receiver. This power may increase if multiple receivers are driven.

eNet-MA4 Power and Ethernet Speed Negotiation:

At least 1500 mAmp @ 5 VDC available power is recommended for eNet-MA4.

Alta's optional J1 cables (e.g. ENETCAB-1553-J1-02) provide a USB connector for the power source. Most computers/USB power adapters provide enough power, but some only provide ~500 mAmps and this may not allow eNet-MA4 to negotiate to 1000 Ethernet (as gigabit Ethernet draws extra power: about 150 mAmps). If using USB power, make sure it can provide at least 1500 mAmp of power (there are many low-cost USB-AC power adapters that provide 1000 or 2000 mAmp of USB 5V power).

For non-USB power connections, make sure to provide 7.5 watts of 5-32 VDC of power.

MTBF

Please contact your Local Sales Representative or Alta Technical Support for additional information regarding any concerns or questions that may arise regarding MTBF for this product.

Environment: Ground Benign, 25C

Table 4. MTBF

eNet-MA4 *	
1 Channel	514,678 hrs
2 Channel	497,349 hrs

*Note: MTBF numbers were calculated with 4 Shared and 8 Receive ARINC Channels. Channel number in table are populated 1553 channels.

eNet-MA4 Photograph

The following picture shows the front side of the eNet-MA4 product.

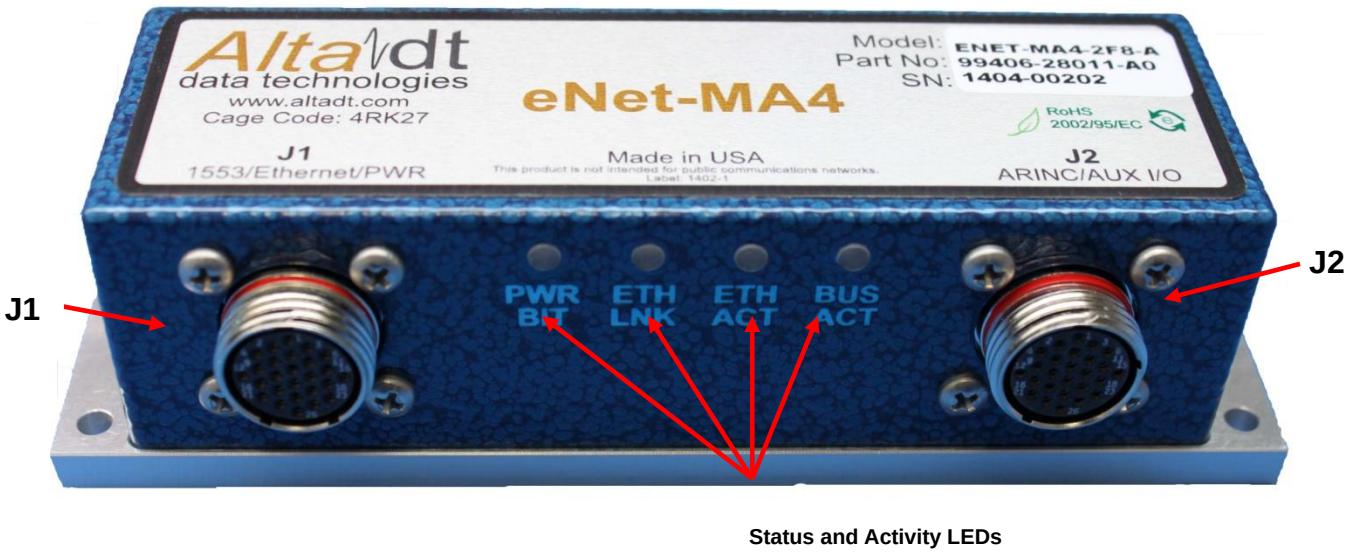


Figure 1. eNet-MA4 Front Side

LED Descriptions

Table 5. LED Descriptions

Name	Description
PWR BIT	Green = Power On, FGPA Loaded and No Bit Errors Detected Green Blinking = IRIG lock is detected. Blinks at 1sec intervals. Red = Power On, FPGA Loaded and Bit Errors Detected, or Factory Reset Asserted
ETH LNK	Green = Ethernet Link Detected Red = No Ethernet Link Detected
ETH ACT	Green = Ethernet Activity Detected without Error Red = Ethernet Activity Detected with Error
BUS ACT	Green = A429/1553 Activity Detected without Error Red = A429/1553 Activity Detected with Error

See Appendix A for J1 and J2 Connector and Optional Cable Information.

eNet-MA4 Dimensions

The following figure provides the dimensions of the eNet-MA4 product. All units are in millimeters.

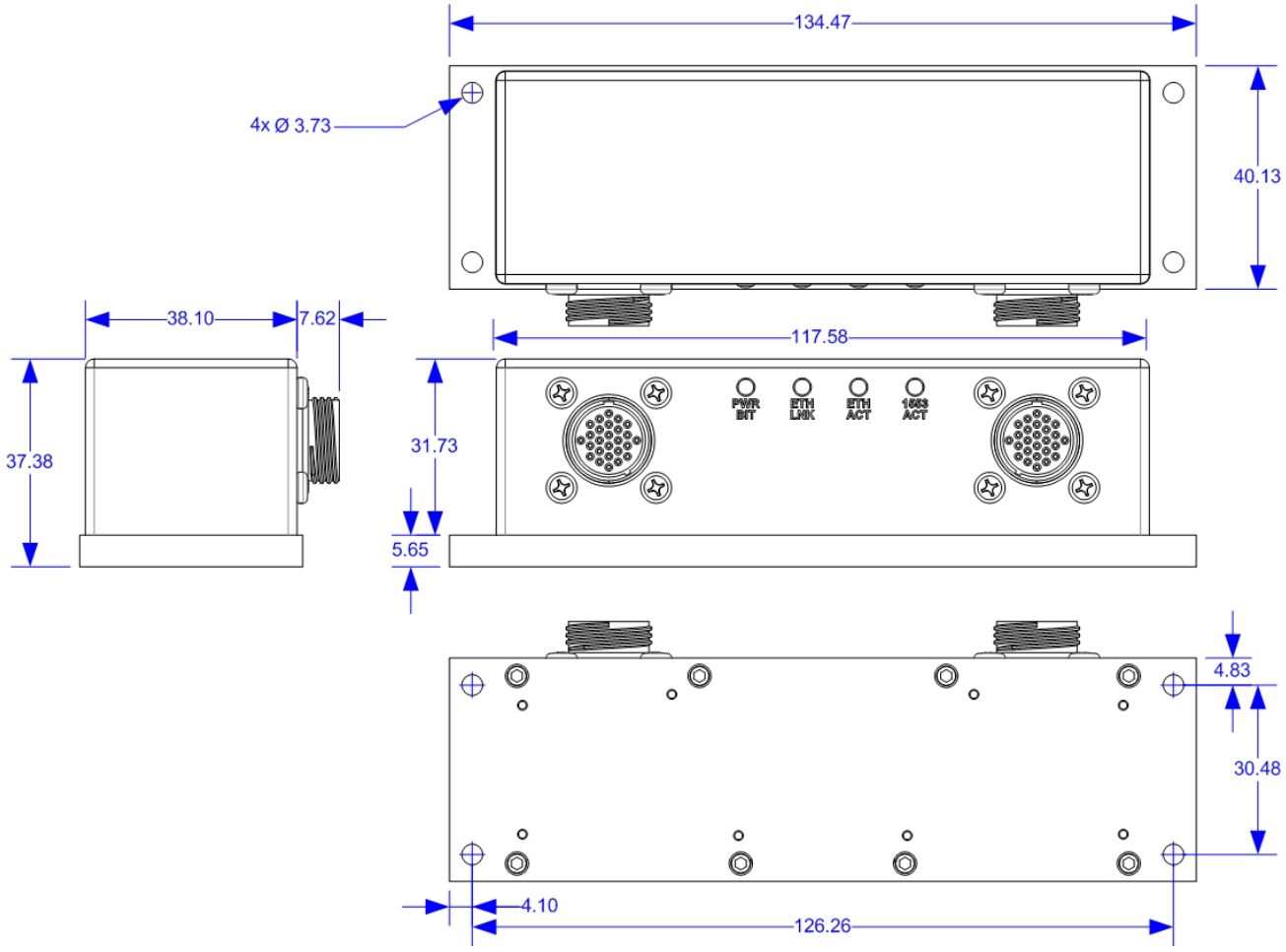


Figure 2. eNet-MA4 Dimensions (mm)

Factory Default IP V4 Address

The factory default IP V4 address for eNet-MA4 is **192.168.0.128**. This value can be re-programmed/flashed by the user as needed for their LAN settings (see Appendix B). Once changed, it is recommended that the user label/tag/record the unit's new IP address for future reference.

How to Reset IP Address to Factory Default

It can be useful to reset the eNet-MA4 IP address back to the factory default (see above) if you have forgotten the device's address (or for other reasons).

To reset eNet-MA4 IP address to the factory default, connect the ~FACTORY IP RESET pin on the J2 connector to ground (see Appendix A) **prior to powering up the unit**. Upon power up with the pin grounded, the device will temporarily have the factory default IP address and the PWR BIT and ETH LNK lights glow red. Keep the pin grounded and now reprogram/flash to a new permanent address per Appendix B (even if you want to keep the 192.168.0.128 default value).

NOTE: When the ~FACTORY IP RESET signal is active (pulled low - grounded) this puts the eNet-MA4 product in programming mode only. The protocol engines will be held in reset and will not be operational in this mode.

MAC Address:

The MAC address for the eNet-MA4 consists of the unique Alta OUI identification number and the serial number of the product.

The first 3 bytes contain the Alta OUI identifier: 94:9C:55
The last 3 bytes contain the last 5 digits of the product type and serial number in hex.

Example Mac address for eNet-MA4 Serial Number 1404-00211:

94:9C:55:A0:00:D3 (where 211 decimal equals D3 hex)

Signal Capture Discussion

The eNet-MA4 HW provides Signal Capture capability on 1553 Channel One (bus A and B) and A429 Bank 1 (channels 1 and 2).

1553 Signal Capture:

The Signal Capture feature uses an analog-to-digital converter (ADC) to capture the electrical signal on the selected 1553 stub (A or B). The Signal Capture feature will capture 2048 samples at a rate of 20MHz, or 50 nanoseconds per sample. Therefore, the sample buffer contains 102.4 microseconds of data. Each sample is an 8-bit (256 step) value representing the differential voltage on the 1553 stub.

To convert the raw ADC data to a stub voltage representation, use the following formula:

Stub Voltage = (ADC data – 128) * 2.5 (xformer ratio) * 32 (voltage divider) * 2mv (step voltage)

Reducing the above gives:
Stub Voltage = (ADC data – 128) * 0.16

WARNING: The Signal Capture Feature does NOT replace a calibrated oscilloscope for voltage or timing measurements on the 1553 stub. The Signal Capture Feature provides simple voltage and timing data. If more precise information is needed regarding the electrical signal on the 1553 bus, a real oscilloscope should be used.

The following steps should be performed to acquire Signal Capture data from the PE. See the *AltaCore-1553* User's Manual for more information on the Signal Capture CSR and Data Registers.

1. Set the Trigger on Any Activity bit in the Signal Capture CSR
2. Wait for Data Ready bit to get set in the Signal Capture CSR
3. Read data from the Signal Capture Data Register. Note: The Data Register contains four samples.
4. Keep reading data until the FIFO Not Empty bit is set to zero by the PE.

A429 Signal Capture:

The Signal Capture Feature uses an analog-to-digital converter (ADC) to capture the electrical signal on the first RX channel of the card. The Signal Capture feature will capture 2048 samples at a rate of 2MHz, or 500 nanoseconds per sample. Therefore, the sample buffer contains 1024 milliseconds of data. Each sample is an 8-bit (256 step) value representing the differential voltage on the first receive channel (RX channel 0). The Signal Capture data should be accurate to within 500mV, but is not calibrated. The FIFO is in words (512 words) with 4 ADC bytes per word (the byte positions are provided below – "Root Signal Data").

To convert the raw ADC data to a stub voltage representation, use the following formula:

Bus Voltage = (ADC data – 128) * 22 (voltage divider) * 2mv (step voltage)

Reducing the above gives:
Bus Voltage = (ADC data – 128) * 0.044

WARNING: The Signal Capture provides simple voltage and timing data and is NOT a calibrated, precision instrument. The Signal Capture Feature does NOT replace a calibrated oscilloscope for voltage or timing measurements.

The following steps should be performed to acquire Signal Capture data from the PE. See the *AltaCore-ARINC* User's Manual for more information on the Signal Capture CSR and Data Registers.

1. Set the Trigger on Any Activity bit in the Signal Capture CSR
2. Wait for Data Ready bit to get set in the Signal Capture CSR
3. Read data from the Signal Capture Data Register. Note: The Data Register is 32-bit word contains four ADC byte samples.
4. Keep reading data until the FIFO Not Empty bit is set to zero by the PE.

Arinc-717 Operation

TX Operation

When a channel is set to transmit in Harvard Bi-Phase mode, this forces a TX channel pair (channels 1&2 and 3&4 pairs) to differentially drive the 0-5V positive leg of the ARINC-429 drivers. The odd channel is the positive differential signal and the even channel leg is the negative differential signal used to create the Harvard Bi-Phase 717 encoding. This setting is provided for older ARINC-717 DFDRS systems. This setting ONLY applies to the first four TX channels.

Table 6. TX Connections for ARINC-717 operation

J2 Pin#	Signal
1	A717 TX1+
5	A717 TX1-
9	A717 TX2+
11	A717 TX2-

RX Operation

When a channel is set to receive in Harvard Bi-Phase mode, the same connector inputs are used as when operating in standard ARINC-A429 mode. This ONLY applies to the first two RX channels. See the ARINC (RX & TX) Protocol Engine Specifications-User's Manual for more information on setting the channel to operate in Harvard bi-phase mode.

Table 7. RX Connections for ARINC-717 operation

J2 Pin#	Signal
1	A717 RX1+
3	A717 RX1-
5	A717 RX2+
6	A717 RX2-

Software Device vs. Physical Channel

The eNet-MA4 can contain one or more ***devices.*** A **device** is 8 channels (RX/TX configuration will vary). Another term for device is **bank. Device = Bank** (This is group of ARINC 8 or less channels)**.** The following table serves as a cross reference between software designation of bank/channel and the cards physical channel.

Table 8. Software vs. Physical Channel

Software Device	Physical Channel
Bank 1, Channel 1	RX1/TX1
Bank 1, Channel 2	RX2/TX2
Bank 1, Channel 3	RX3/TX3
Bank 1, Channel 4	RX4/TX4
Bank 1, Channel 5	RX5
Bank 1, Channel 6	RX6
Bank 1, Channel 7	RX7
Bank 1, Channel 8	RX8

A429 Shared Channels

The eNet-MA4 provides some shared RX/TX channels which include a line-driver for transmit operation and a receiver connected to the same pins. Under normal powered operation for a shared channel, the transmit channel may either drive the bus or act as high impedance allowing for receive operation. When the eNet-MA4 does not have power applied during normal operation, the line-driver acts as a low impedance source on the bus which can cause the bus voltage to fall below the minimum of the ARINC 429 specification if an external device is transmitting on the bus. In this case, Alta recommends that RX only channels be used for the application instead of shared channels.

Host Memory Map

The figure below shows the basic memory map configuration for an eNet-MA4 interface with one megabyte of RAM per channel. Special configurations may vary.

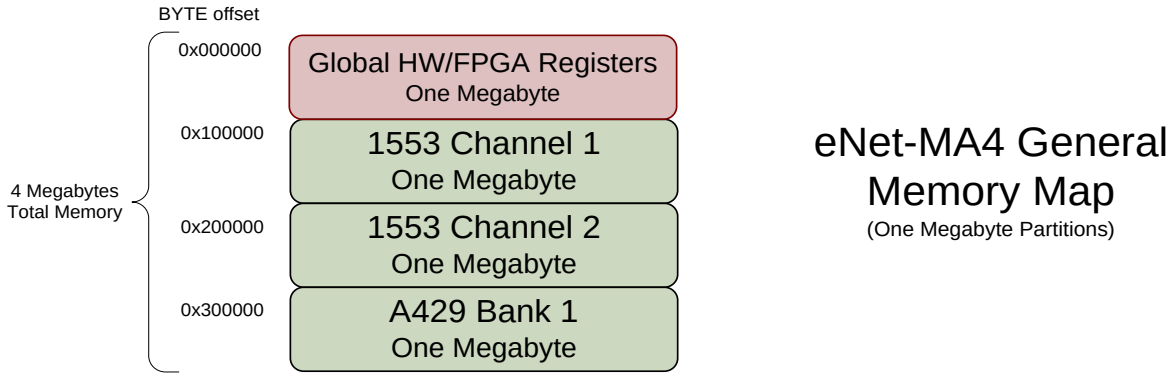


Figure 3. Basic Memory Map

eNet-MA4 Global Registers

The first Megabyte of the eNet-MA4 memory map contains backplane and global card level settings and status values that affect processing for all channels. Details on Global Registers may be found in the AltaCore-1553 Spec User's Manual or AltaCore-ARINC Spec User's Manual.

Channel Operation

For more information regarding individual channel operation, please see the *AltaCore-1553* User's Manual or *AltaCore-ARINC* User's Manual.

Revision Information

Date	Rev	Description
2014.04.29	A0	Initial Release
2014.08.20	A1	Updated J2 cable table & cable names.
2014.11.10	A2	Updated Alta PN for mating connectors.
2015.02.24	A3	Added Software Device vs. Physical Channel table
2015.05.08	A4	Updated card level specification bullets to remove RS-422 clock & clarify 1760 Ext RT Address as a hard wired build option.
2015.12.10	A5	Updated Pin-Out table
2016.02.10	A6	Added Blinking to PWR/BIT LED, changed bus activity to transmitting in power chart, and various typos.
2016.02.15	A7	Added additional optional cable assemblies and updated signal capture section
2017.01.31	A8	Updated cable information to include whether or not cables contain PVC
2017.05.09	A9	Added A429 Shared Channels paragraph and fixed minor typo
2018.07.20	A10	Added bullet about shared channels to specifications
2018.08.16	A11	Changed default IP address listed in Appendix B, Step 2 from 192.169.0.128 to correct 192.168.0.128
2018.11.06	A12	Updated file name in Appendix B
2021.03.02	A13	Updated LED PWR BIT description (table 5) to include factory reset asserted and updated Card Level Specifications on pg.3 to include Reverse Polarity Protection
2023.01.17	A14	Updated Card Level Specifications, NAICS #, contact info and PWR table Removed revision and date from board description

Appendix A
eNet-MA4 Connectors and Cable Assembly Information

J1 and J2 Connector Information – Alta & Glenair (GA) Part Numbers

http://www.glenair.com/index.htm

26-Pin Connectors are Keyed with “A” or “B” pattern. J1 uses the A pattern.

- **J1 Jack:** Glenair 801-011-02M10-26SA Female
  - **Glenair Mate/Male Part Number: 801-007-16M10-26PA**
  - Alta J1 Mate Part Number: ENETCON-J1-01
- **J2 Jack:** Glenair 801-011-02M10-26SB Female
  - **Glenair Mate/Male Part Number: 801-007-16M10-26PB**
  - Alta J2 Mate Part Number: ENETCON-J2-01
- Dust Cover Plug (probably only needed for J2, but wo
- rks with either connector):
  - **Glenair Part Number:** 667-218-M-N10
  - Alta Mate Part Number: ENETDCAP-J2-01

J1 and J2 Pin-Out Table

J1 Pin-Out	Signal	J2 Pin-Out	Signal
1	ETHERNET BI_DA+	1	A429 RX1+/TX1+
2	DC POWER IN +	2	A429 RX5+
3	ETHERNET BI_DA-	3	A429 RX1-/TX1-
4	1553 CH 1A+	4	A429 RX5-
5	1553 CH 2A+	5	A429 RX2+/TX2+
6	ETHERNET BI_DB+	6	A429 RX2-/TX2-
7	1553 CH 2A-	7	A429 RX6+
8	1553 CH 2B+	8	SDISC1
9	1553 CH 1A-	9	A429 RX3+/TX3+
10	JTAG 3.3V OUT	10	TRIGGER IN
11	ETHERNET BI_DB-	11	A429 RX4+/TX4+
12	1553 SHIELD \ CHASSIS GND	12	A429 RX3-/TX3-
13	DC POWER RETURN -	13	A429 RX6-
14	1553 CH 2B-	14	SDISC2
15	GND	15	A429 RX4-/TX4-
16	1553 CH 1B+	16	A429 RX7+
17	JTAG TDI	17	TRIGGER OUT
18	ETHERNET BI_DC+	18	GNDC
19	JTAG TDO	19	A429 RX7-
20	JTAG TRST	20	A429 RX8+
21	1553 CH 1B-	21	IRIG IN
22	JTAG TMS	22	GND
23	ETHERNET BI_DC-	23	~FACTORY IP RESET
24	JTAG TCLK	24	~TX INHIBIT
25	ETHERNET BI_DD+	25	A429 RX8-
26	ETHERNET BI_DD-	26	EXT CLK I/O – TTL I/O

Optional J1 and J2 Cable Assemblies

eNet-MA4 has two Glenn Air circular connectors labeled J1 (left-most) and J2 (right-most). Connector part numbers and pin-outs are provided in the eNet-MA4 hardware manual. Alta provides optional cable assemblies for these connections and their part numbers are:

- **ENETCAB-1553-J1-02** (J1 with two 1553 channels**)
- **ENETCAB-1553-J1-01** (J1 with one 1553 channel**)
- **ENETCAB-J1-01** (J1 with no 1553**)
- **ENETCAB-J2-01** (J2 cable assembly**)

These cables are all IPC-610 Class 3/RoHS.

* Does not contain PVC

** Contains PVC

The ENETCAB-1553-J1-02 assembly is show below:

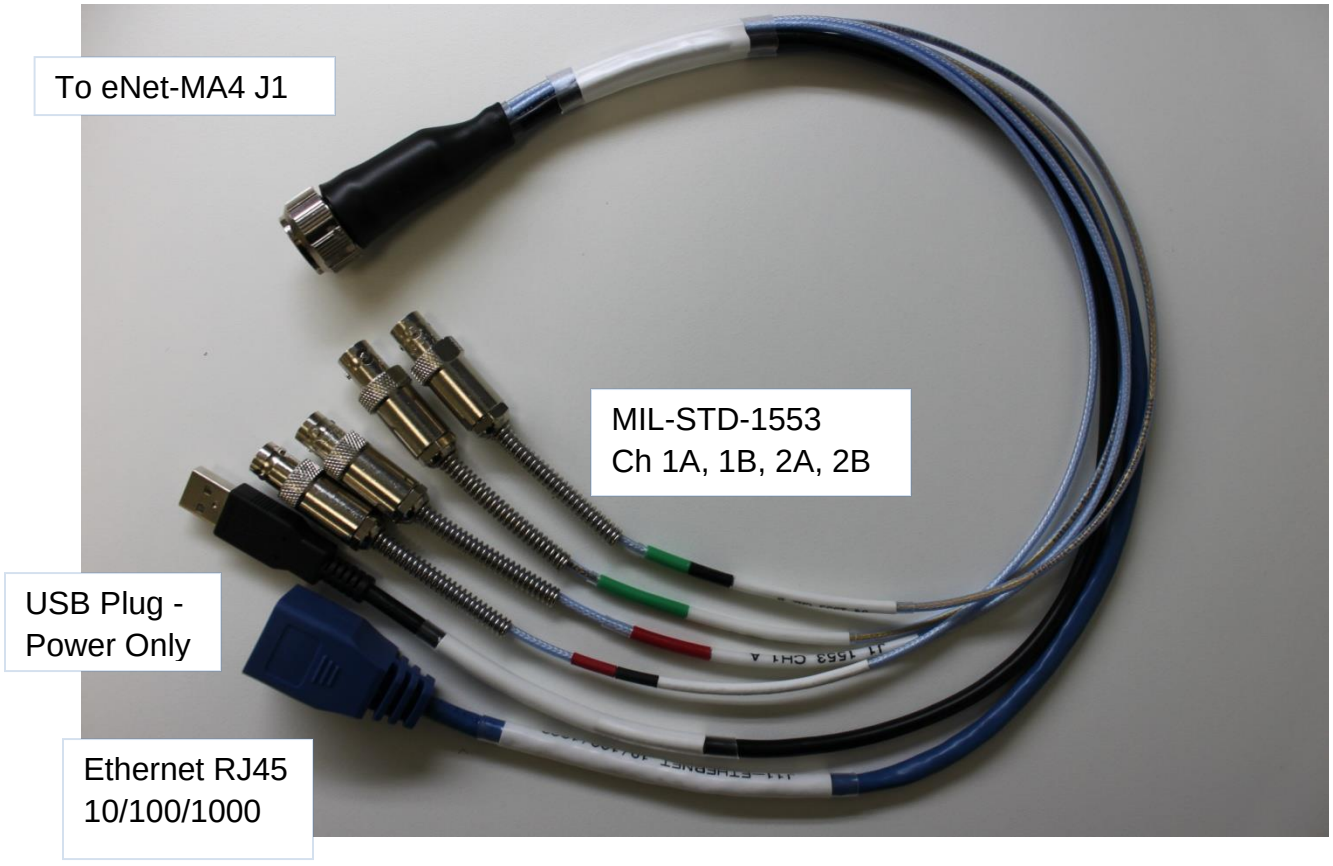
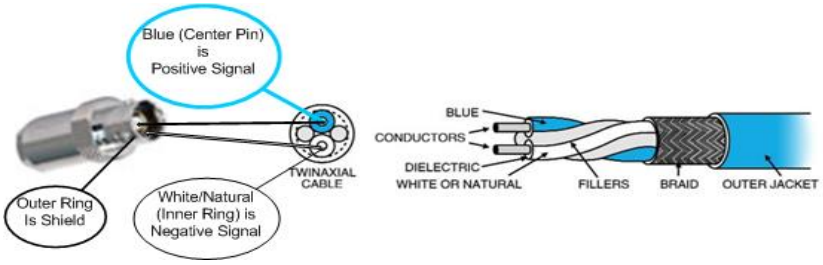


Figure A-1: Optional ENETCAB-1553-J1-02 Cable Assembly

ENETCAB-1553-J1-02 Cable Assembly Notes:

- **Cable Length: Approximately 2ft**
- **Cable Assembly P1 and Part Number Label Approximately 2” from Base**
- **1553 Connectors are 3-Lug (BJ-77 Type) Female Connectors**
  - ~1” from Connector Base Label: Cable Part Number
    - CH1 A – Shrink Tube Color Code: Red
    - CH1 B –Shrink Tube Color Code: Red-Black Stripe
    - CH2 A – Shrink Tube Color Code: Green
    - CH2 B –Shrink Tube Color Code: Green-Black Stripe
    - J1 1553 CH1 A
    - J2 1553 CH1 B
    - J3 1553 CH2 A
    - J4 1553 CH2 B
  - 1553 thin MIL Cable



- **Ethernet RJ45 Jack**
  - Depending on your computer, you may need a Cross-Over Cable Per T568B. Most computers auto negotiate and standard Patch Cables work. Recommend the following references:
    - http://en.wikipedia.org/wiki/Ethernet_physical_layer
    - **CAT 6 CABLE STRONGLY RECOMMENED**
    - Straight-Through Ethernet Cable Pin Out for T568B Per Table:

RJ45 Pin #	Wire Color (T568B)	Wire Diagram (T568B)	10Base-T Signal 100Base-TX Signal	1000Base-T Signal
1	White/Orange		Transmit+	BI_DA+
2	Orange		Transmit-	BI_DA-
3	White/Green		Receive+	BI_DB+
4	Blue		Unused	BI_DC+
5	White/Blue		Unused	BI_DC-
6	Green		Receive-	BI_DB-
7	White/Brown		Unused	BI_DD+
8	Brown		Unused	BI_DD-

Table A-1: Over-Molded Ethernet RJ-45 Jack Pin-Outs

- **USB Standard A Plug Connector (fits most computer USB jacks).**
  Power Only (“Functional Decoration” Device).
    - Recommend >1000 MA source, which is common on most computers, but low-end wall adapters may not provide enough power (recommend wall adapter ratings at 2000+ MA).
    - Pin 1 +5 Vcc; Pin 4 Ground; Data Pins 2 & 3 Not Connected

The ENETCAB-J2-01 assembly is show below:

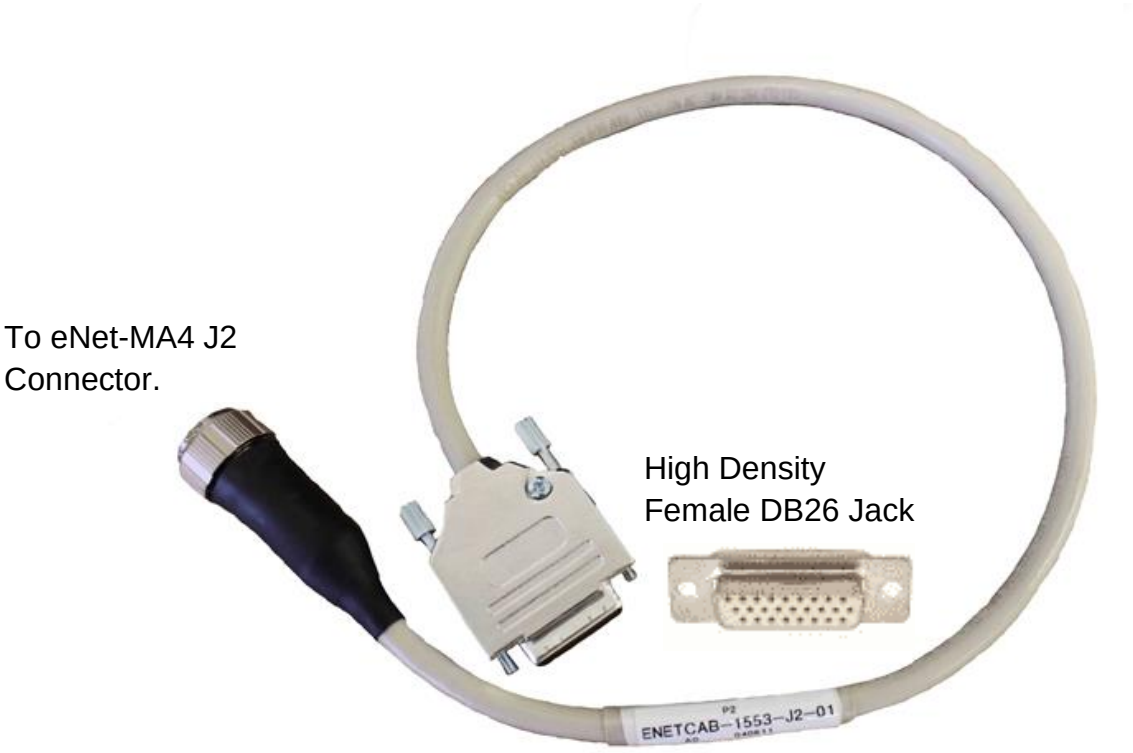


Figure A-2: Optional ENETCAB-J2-01 Cable Assembly

ENETCAB-J2-01 Cable Assembly Notes:

- **Cable Length: Approximately 2ft**
- **Cable Assembly Part Number Label Approximately 2" from Base**
- **J1 DB26 Female High Density Connector** (Pin Numbers Marked on Connector)
    - ~1" from DB Connector - J1 Label

J2 Circular Pin	Signal	Cable Color	DB-26
1	A429 RX1+/TX1+	Blue/Green	1
2	A429 RX5+	Yellow/Green	3
3	A429 RX1-/TX1-	Green/Blue	2
4	A429 RX5-	Green/Yellow	6
5	A429 RX2+/TX2+	Brown/Green	5
6	A429 RX2-/TX2-	Green/Brown	4
7	A429 RX6+	Black/Red	7
8	SDISC1	Black/Blue	11
9	A429 RX3+/TX3+	Green/White	15
10	TRIGGER IN	Red/White	8
11	A429 RX4+/TX4+	Green/Red	12
12	A429 RX3-/TX3-	White/Green	16
13	A429 RX6-	Red/Black	14
14	SDISC2	White/Red	17
15	A429 RX4-/TX4-	Red/Green	13
16	A429 RX7+	Black/Yellow	18
17	TRIGGER OUT	Blue/Black	10
18	GNDC	Shield	Case
19	A429 RX7-	Yellow/Black	19
20	A429 RX8+	Brown/Black	20
21	IRIG IN	Blue/Red	21
22	GND	Red/Blue	22
23	~FACTORY IP RESET	Green/Black	23
24	~TX INHIBIT	Black/Green	24
25	A429 RX8-	Black/Brown	26
26	EXT CLK I/O – TTL I/O	Black/White	25

Table A-2: J2 DB26 Pin-Outs

Appendix B
Setting eNet-MA4 Static IP V4 Address

The **eNet-MA4** device has a factory default IP V4 address of 192.168.0.128 (class.class.subnet.hostNum) loaded in the boot-up flash. Many customers need to change this address to match their computer's or LAN IP settings (especially subnet). Changing the eNet-MA4 IP address in boot-up flash is reviewed in this Appendix.

The customer has two options to change the IP address: Use the AltaView application for Microsoft Windows™, or compile and execute a C sample program provided on the CD/Web distribution in the "Examples" "M1553" folder (this option is needed for Linux, VxWorks or other non-Windows systems). Even for non-Windows environments, it may be easier to find a Windows machine and make the address change prior to installing eNet-MA4 on your Linux/Other/LAN environment.

Alta can also preset the IP address for you at the factory. This may entail a special part number for re-order tracking, but we would be glad to help if you need the device's address programmed or preset. Contact Alta at alta.support@altadt.com

Windows Systems
The following paragraphs apply only to Windows machines using AltaView to update the IP Address, but Linux/Unix/Other users may want to briefly review. Non-Windows users will need to read their respective Appendix in the AltaAPI manual for their OS and will then need to compile/execute the example program ADT_L1_Global_ex_eNet_Set_IP.c. **You will need to set your local computer's IP address to match the 192.168.0.X class.class.subnet settings of eNet-MA4 default to communicate to the device (X = host number and can be anything other than eNet-MA4 default value of 128).**

STEP ONE: Set your Windows Computer's Static IP V4 Address to Match eNet-MA4 class.class.subnet Values.

> **IT IS RECOMMENDED TO TURN OFF WIRELESS DEVICES ON THE CLIENT COMPUTER WHEN USING eNet-MA4. Windows OS can send packets on different network interfaces, so you should turn off all network connections except the wired device that is connected to eNet-MA4. You can also HARD SET the Windows Routing Table for eNet-MA4 IP routing, but this is beyond the scope of this discussion (google: Setting Windows IP Routing Table).**

There are numerous web sites that detail how to set your Windows machines IP address to a static value. Simply google: Setting a Static IP address in Windows.

Here are general instructions (do not have eNet-MA4 connected to your computer yet).

1. Open the "Start" menu and click Control Panel. Click the "Network and Internet" link, then the Network and Sharing Center link. Click the "View network connections" link to see your adapters.

2. Right-click on the adapter you want to change and choose "Properties" from the list. Click once on "Internet Protocol Version 4 (TCP/IPv4)" to select it and then click the "Properties" button. Click the radio button for "Use the following IP address:" and enter the static IP address you'd like to use in the IP address box.
    a. **IP Address Example: 192.168.0.10**
    (This will match eNet-MA4 Default Subnet)

3. Hit "Tab" when you are finished and the "Subnet mask" value should auto-fill with the proper value. You should not need to set any other values (like gateway – leave blank).
    a. **Example SubNet Mask: 255.255.255.0 –** Should Auto Fill In

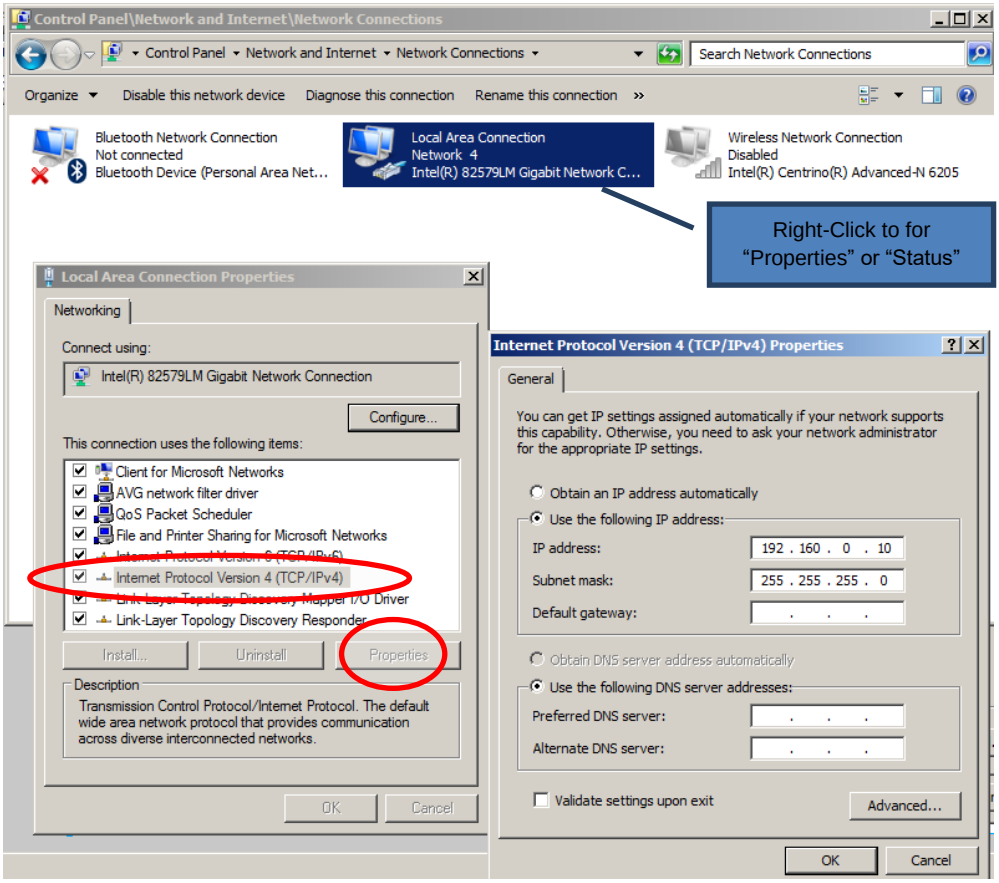
4. Click "OK" to save your changes, then click "OK" again on the Local Area Connection Properties window.
    a. You can verify your computer's setting by right-clicking on the "Local Area Connection" device and selecting "Status" -> "Details"

5. **Now Connect eNet-MA4 to your computer using a standard Ethernet CAT6 patch cable.** (CAT5 will probably work, but we recommend CAT6 – **a cable is provided in the eNet-MA4 shipment box**).

6. Optional: Now you can communicate to eNet-MA4 using AltaView or the C example programs.

Windows Example for Setting Static IP V4 Address

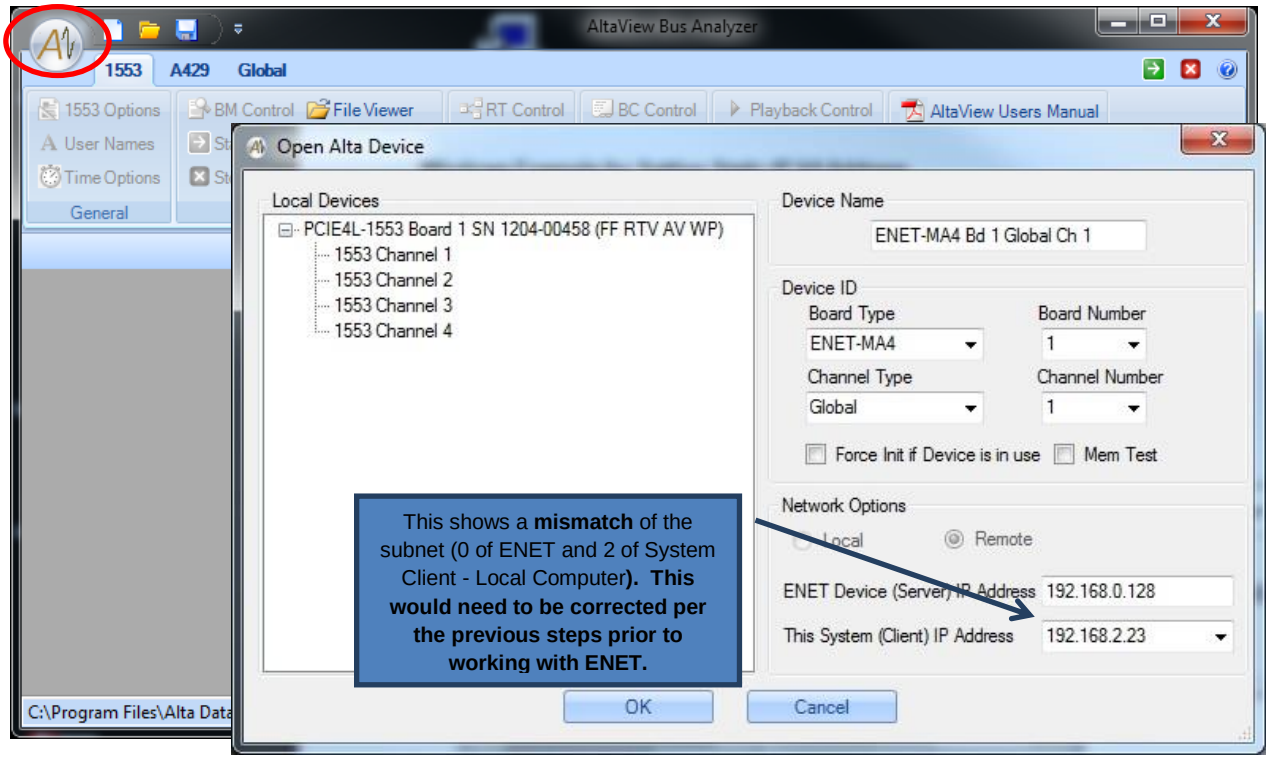
Program Windows Static IP Address on Windows 7 (right click on Local Area Connection, Network 4 in this example, and select properties). When **Internet Protocol Version 4** is selected, the **Properties** button should brighten and be selectable.



STEP TWO: Use AltaView to Set eNet-MA4 IP Address.

With the latest Alta software installed on your computer, perform the following steps:

1. Open AltaView
2. Click on **Alta Logo Button** and Select "**Open Device**"
    a. On the right-middle pull down menus, select from default:
    Board Type: ENET-MA4
    Channel Type: Global
3. In the lower right **Network Options** area, make sure the "ENET DEVICE (Server)" and "This System (Client)" IP Address are correct. The ENET Server IP address should be default 192.168.0.128 and your computer's Client address should be 192.168.0.X, where X is anything but 128 (eNet-MA4 host number address).
    a. If you can't select your Client's IP address to the proper subnet value, then you have a problem with setting the computer's static IP address. Go back to Step One or contact your administrator for help. You need to verify that the Local Area Connection device is set to the proper 192.168.0.X static IP address.



4. Once you get the IP addresses (and subnet) all matching, and the patch cable is connected, then you should successfully open the “Global Ch 1” device.
    a. In the Device Window you should see **Status** **Initialized**
5. Now you can Click on **Global** Tab (next to 429) and then select **ENET Config**.
6. Then simply set the New IP Address and click OK (do not set APMP at this time unless you want to enable auto BM APMP – not reviewed here).
7. There should be short pause and then you will get a SUCCESS Window followed by a COMPLETE window. Close AltaView if you want.
    a. NOW CYCLE POWER on the eNet-MA4 device and it should be ready for use with the new IP Address. If you mess up, no worries, you can reset the address to the factory default by grounding the J2 connector pin ~FACTORY IP RESET. See the previous section of this manual for pin-out info.
    b. You probably want to set your computer’s IP address back to another value or back to DNS (auto IP addressing).